# **RL0607**

## Unshielded radial leaded drum core inductors



#### **Product features**

- ver win tir g

  ge from 6.8 µH to 1500 µH

  ange from 0. 2 A to 2.23 A

  mm OD x 7.2 mm through-hele
  package

  Fe file vove material

#### **Applications**

- · LED Drivers and lighting
- · Utility meters
- · Appliances and white goods
- Motor drives
- Power supplies
- General pulpose fistering

#### Environmen an data

- Sto age temperature range (Compo 40 °C to +125 °C
- Operating temperature range. 40 °C to +125 °C (ambient plus self-temper iture rise)









### **Product specifications**

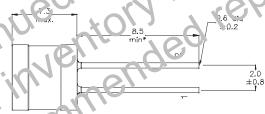
Part Number <sup>4</sup>	OCL¹ (μΗ) ±10%	I <sub>rms</sub> <sup>2</sup> (A)	I 3 (A)	DCR (Ω) @ +20 °C max.	SRF (MHz) typ.
RL0607-6R8-R	6.8 ± 20%	2.23	1.82	0.038	26
RL0607-100-R	10	1.82	1.51	0.058	21
RL0607-180-R	18	1.52	1.13	0.083	16
RL0607-330-R	33	1.08	0.840	0.171	11
RL0607-470-R	47	0.953	0.690	0.217	8
RL0607-820-R	82	0.686	0.530	0.426	6
RL0607-151-R	150	0.520	0.390	0.730	4
RL0607-221-R	220	0.423	0.320	1.10	3
RL0607-471-R	470	0.306	0.220	2.00	2
RL0607-821-R	820	0.219	0.170	4.13	2
RL0607-102-R	1000	0.205	0.150	4.70	1
RL0607-152-R	1500	0.166	0.120	7.20	1

- 1. Open Circuit Inductance (OCL) Test Parameters: 10 kHz, 0.1  $V_{\rm rms^4}$  0.0Adc, +25 °C
- 2. I<sub>rms</sub>: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.
- 3.  $I_{\text{sat}}$ : Peak currer , for app ox mately 5% rolloff at +25 °C
- 4. Part Number Definition: RL0607-yyy-R
  - RL060 ' = Product code and size
  - vy /= nauctance value in μH, R = decimal point
  - in no R is present then third character = number or zeros.
  - '-B" suffix BaHS compliant

#### **Dimensions - mm**

max

Top view Si



Recommended pad layout

Schematic



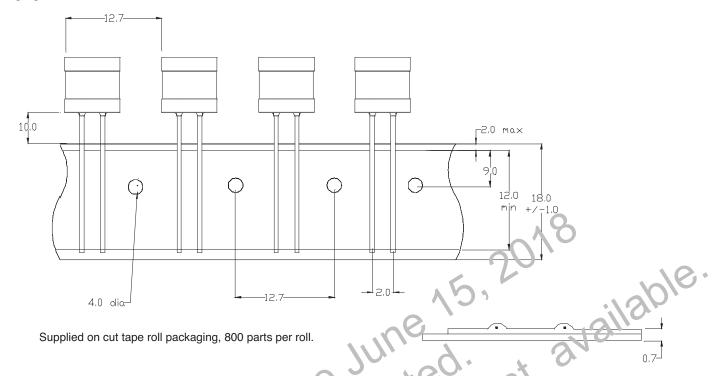
Part marking: 1 'xx

1 = P'\_^6u7

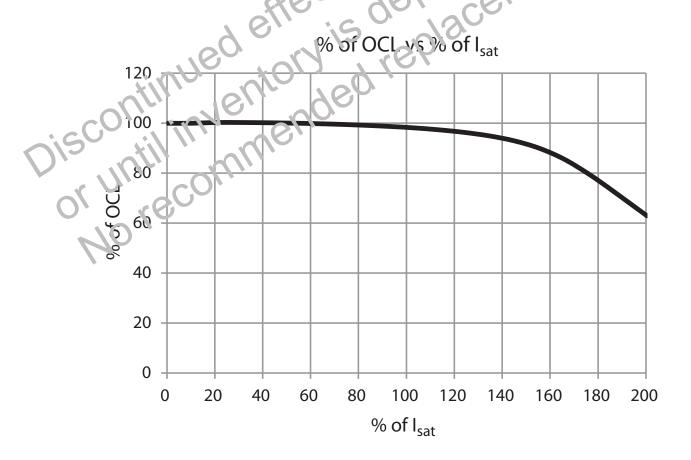
xxx : ino iclance in  $\mu$ H, R = Je ina' point; if there is no R, then third character = number of zeros wly = Jule code, R = revision level.

\*Lead length is after the components are trimmed from the packaging tape roll. Do not roughthat r = r + r or vias underneath the inductor

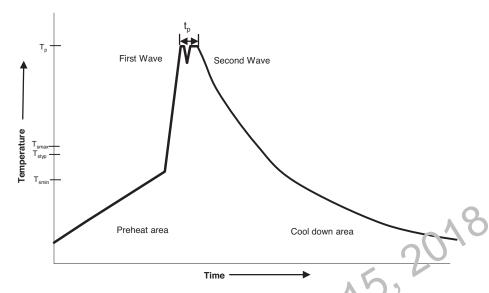
#### Packaging information - mm



#### **Inductance characteristics**



#### Wave solder profile



Reference	61760	4.2006

Preheat area	Cool down area						
		00,					
Time -	<b>→</b>						
	A V						
Reference EN 61760-1:2006		.\/					
Profile Feature	Standard SnPb 50 der	Lead (Pb) Free Solder					
Preheat	.00	1.0					
Temperature min. (T <sub>smin</sub> )	100°C	100°C					
Temperature typ. (T <sub>styp</sub> )	120°C	120°C					
Temperature max. (T <sub>smax</sub> )	1,30°C	130°C					
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	7J seconds	70 อานาทนร					
$\Delta$ preheat to max Temeperature	150°C na:	5℃ max.					
Peak temperature (T <sub>p</sub> )	13(C-1:00°C	250°C - 260°C					
Time at peak temperature (t <sub>n</sub> )	10 seconds max	10 seconds max					
Time at peak temperature V <sub>p</sub> ,	5 seconds r. a: euch wave	5 seconds max each wave					
70 (3	~ 2 K/s min	~ 2 K/s min					
Ramp-down rate	~3.5 K'S VD	~3.5 K/s typ					
	~'ıksrıax	~5 K/s max					
Time 25.70 . 7 / 5°C	4 minutes	4 minutes					
n'arual solder							
350°C, 4-5 second it (by soldering irm), ter erally manual, hand soldering is not recommended.							
713 111 1							
1), '0//, '0//,							
* O, CO,							
Or O'RECOILL							
10							
No							
		pport devices or systems without the express written					
	systems are devices which support or su	ustain life, and whose failure to perform, when properly					

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